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IMPACT OF SAMPLING IN RECURRENCE BAND UTILIZING SOFTWARE DEFINED RADIO THROUGH MATLAB

Gagandeep Kaur
Assistant Professor (E.C.E.), Yadavindra College of Engineering, Punjabi University Campus, Talwandi Sabo, Bathinda, Punjab

ABSTRACT
Two social affairs of people with different sorts of standard radio couldn't pass on account of incongruence. The need to talk with people using different kinds of equipment should be settled by using Software Defined Radio (SDR). While the multi-rate Digital Signal Processing (DSP) in the recently referenced correspondence systems serves to offer additional levels of chance in the arrangement of the gatherer, another critical class of multi-rate structures is used at the transmitter side in order to introduce the abundance in the data stream. This overabundance generally serves to energize the evening out of the strategy by convincing a particular design on the communicated sign. If the channel is dark, this strategy helps with recognizing it; if the channel is inadequately shaped; additional overabundance avoids genuine racket heightening at the recipient, and so on. In the second piece of the proposition, we focus on this second assembling of multi-rate structures, decide a bit of their properties, and present certain overhauls in the correspondence systems being alluded to SDR is a far off contraption that works with any correspondence system, be it a cell phone, a pager, a WI-FI handset an AM or FM radio, a satellite exchange, etc. In both equipment and programming DSP procedures are used to design a continuous structure. For SDR applications including high multifaceted design, it gets hard to stay with a solitary assessing rate hence changing the testing rate at different stages is needed for negligible exertion DSP hardware. Subsequently, factor reviewing rates for instance Multirate Digital Signal Processing is required.

Keywords: Software defined radio, digital signal processing, up-sampling, down-sampling, multirate digital signal processing.

INTRODUCTION
The future far off condition is depended upon to include different radio access estimates that give customers an assorted level of adaptability and information transmission. The prevalent reconfigurability and re-programmability of programming characterized radio (SDR) had made itself transform into the most reassuring development to recognize a particularly versatile radio structure. A SDR can be considered as an open designing that makes a correspondence stage by partner modularize and standardized versatile gear building squares. Programming portray task and interconnects between the squares and gives a character to the system. SDR structure portrays SDRs as "radio that gives programming control of a grouping of guideline strategies wide and restricted band action, correspondence security limits and wave structure essential of current and creating standards over a broad repeat run". SDR thought ensures the principal answer for supporting countless distant correspondence benefits in a singular arrangement. There are various applications where the indication of a given analyzing rate ought to be changed over into a similar sign with another testing rate. For example, in electronic sound, there are three unmistakable analyzing rates in the picture 32 kHz in imparting, 44.1 kHz in a high-level limited plate, and 48 kHz in mechanized sound tape. This is the spot multirate advanced sign preparing comes into the picture. There are various employments of the multi-rate progressed sign getting ready and they are being recorded underneath.

(a) High-quality data getting, and limit structures are dynamically taking ideal conditions of the multi-rate strategy to avoid the use of expensive foe of partner straightforward channels and to manage capably signals of different reviewing frequencies

(b) in talk taking care of, multi-rate propels are used to diminish the additional room and transmission speed of talk data. Assessments of talk boundaries are handled at an incredibly low inspecting rate for limit or transmission. Exactly when required, the primary talk is repeated from the low piece depiction at much higher rates using the multi-rate approach (Principe et. al. 2008).

(c) the sensible, significant standards ADC (18, 20, 24 pieces) being utilized today uses multi-rate arrangement. For example: C5532X (Crystal Semiconductor), DSP56ADCX (Motorola) and

(d) multi-rate taking care of has found continuously huge applications in the capable execution of DSP limits. For example, execution of narrowband FIR channels using standard DSP processors is a huge issue considering the way that such channels required. Multirate advanced sign handling is especially useful in quick data acquiring and limit. It lessens the necessity for the expensive adversary of partner straightforward channels and allows the sign planning with a variable analyzing rate. As the cost of quick parts grows dramatically with examining rate, multirate computerized signal Processing licenses equivalent low speed getting ready endeavors, achieving in everyday quick. As such, it terribly reduces the expense and empowers filtering at a low testing rate, in this way diminishing channel solicitations and along these lines computational complex nature. Finally, it changes and channelization for SDR (Hentschel and Fettweis, 2000).

Discrete Fourier Transform as Linear Transform

The finite duration discrete frequency signal can be obtained by sampling one period of continuous spectrum $X(\omega)$ is referred as Discrete Fourier Transform (DFT). The Sampling is carried at N equally spaced points in one period between $0 \le \omega \le 2\pi$ (Equ 1.1 & 1.2).

The Fourier Transform of discrete time signal $x[n]$ is $X(\omega)$ then DFT is denoted by $X(k)$.

$$\therefore X(k) = X(\omega)/\omega = \frac{2\pi}{N}k \quad \text{.......... (1.1)}$$

k=0, 1, 2,..N-1

Where $\frac{2\pi}{N}k$ denotes N equally spaced points over one period to 2, each frequency sample is located by $k = 0,1,2,\ldots\ldots, N-1$.

Thus, N point DFT can be defined as

$$X(k) = X(\frac{2\pi}{N}K) = \sum_{n=0}^{N-1} x[n]e^{-j2\pi nk/N}$$

k=0,1,2,…..N-1

1.2

and IDFT can be defined as (Eq. 1.3)

$$x[n] = \frac{1}{N}\sum_{k=0}^{N-1} X(k).e^{-j2\pi nk/N} \qquad n = 0,1,2,\ldots\ldots N-1 \qquad 1.3$$

These DFT and IDFT formulae may be expressed as (Eq .1.4)

$$X(k) = \sum_{n=0}^{N-1} x[n]W_N^{-nk}$$

$$;k = 0,1,2,3,\ldots\ldots\ldots.N-1 \qquad 1.4$$

$$or, x[n] = \frac{1}{N}\sum_{k=0}^{N-1} X[k]W_N^{-nK} \quad ; n = 0,1,2,3\ldots\ldots\ldots N-1 \qquad 1.5$$

$$where W_N^{nk} = e^{-j2\pi nk/N}$$

$$\Rightarrow W_n = e^{-j2\pi/N} \qquad 1.6$$

$$\Rightarrow W = e^{-j2\pi} \tag{1.7}$$

W is a complex number and is called as the twiddle factor in DFT.

1.2 *Sampling Rate Alteration Scheme (Up/ Down Samplers)*

The block-diagram representation of up-sampler also called a sampling rate expander. An up sampler with an up sampling factor L, where L is a positive integer, develops an output sequence $x_u[n]$ with a sampling rate that is L times larger than that of the input sequence x[n] (Fig.1).

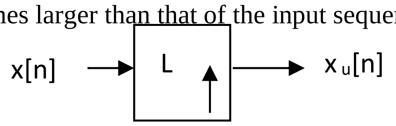

Fig.1. Up-sampler Block [2]

The down sampler is also called a sampling rate compressor. Its block diagram representation is shown next. An down sampler with a down sampling factor L, where L is a positive integer, develops an output sequence $x_u[n]$ with a sampling rate that is L times smaller than that of the input sequence x[n] (Fig. 2).

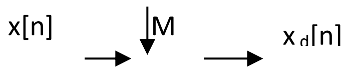

Fig. 2. Down-sampler Block [2]

The up-sampler and down-samplers are linear but time varying discrete time systems. The up-sampler and down-sampler building blocks are often used together in several applications involving multirate DSP. Furthermore, one application of using both type of sampling rate alteration devices is to achieve a sample rate change by a rational number rather than an integer value.

If we do a factor-of-2 sampling rate expansion, it leads to a two-fold repetition of $X(e^{j\omega})$ indicating that Fourier transform is compressed by 2 (Fig. 3).

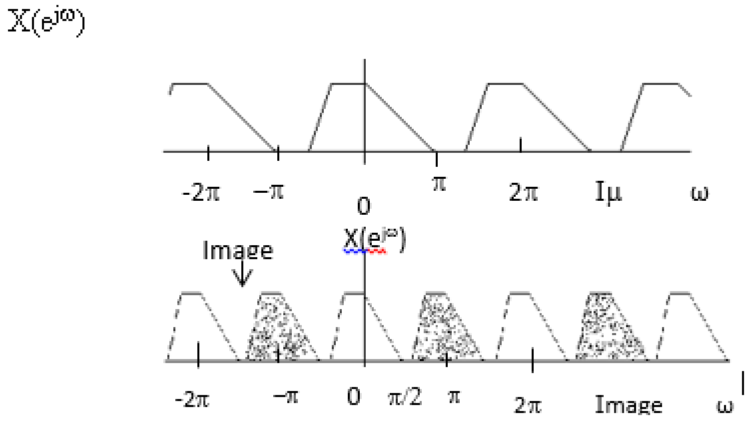

Fig. 3. The effect of up-sampling [18]

This phenomenon is called imaging because we get an additional “image” spectrum. In case of a factor-of-L sampling rate expansion there will be N-1 additional images. Hence a spectrum band limited to low frequency range does not look like a low frequency spectrum after up sampling. But low pass filtering removes the L-1 images and in effect fills in the zero-valued samples by interpolated sample values.

When we down sample, the spectrum overlap, this overlap is called aliasing (Fig. 4).

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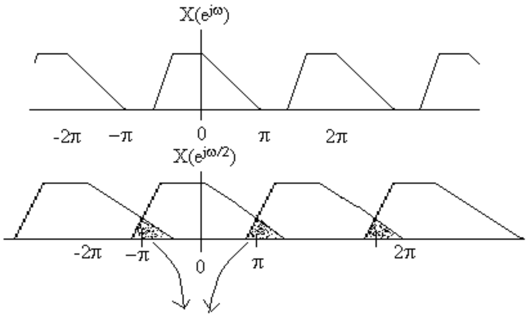

Fig. 4. The effect of down-sampling [18]

Aliasing due to a factor-of-M is absent if and only if signal x[n] is band limited to □□□□□

The basic sampling rate alteration devices can be used to change sampling rate by an integer factor only. To bring about a fractional change you need to cascade down-sampler and up-sampler.
The condition that a factor-of-l up-sampler and a factor-of-m down-sampler can be interchanged is that m and l have to be relatively prime that is m and l have no common factor k>1.

k=0, 1, 2,..N-1

DESIGN AND DEVELOPMENT OF ALGORITHM

In many practical applications of digital signal processing, one is faced with the problem of changing the sampling rate of a signal, either increasing it or decreasing it by some amount. For example, in telecommunication system that transmits and receives different type of signals (e.g. teletype, facsimile, speech, video, etc.), there is requirement to process the various signals at different rates commensurate with the corresponding bandwidths of the signals. The process of converting a signal from a given rate to a different rate is called sampling rate conversion. In turn, systems that employ multiple sampling rates in the processing of digital signal are called Multirate Digital Signal Processing Systems.

Sampling rate conversion of a digital signal can be accomplished in one of two general methods. One method is to pass the digital signal through a D/A converter, filter it if necessary, and then to resample the resulting analog signal at the desired rate (i.e. to pass the analog signal through an A/D converter). The second method is to perform the sampling rate conversion entirely in the digital domain.

One apparent advantage of the first method is that the new sampling rate can be arbitrarily selected and need not have any special relationship to the old sampling rate. A major disadvantage, however, is the signal distortion, introduced by the D/A converter in the signal reconstruction, and by the quantization effects in the A/D conversion. Sampling rate conversion performed in the digital domain avoids this major disadvantage.

Software Defined Radio (SDR) System:

A SDR framework can deteriorate in two significant parts

1. Hardware functional block and
2. Software functional block

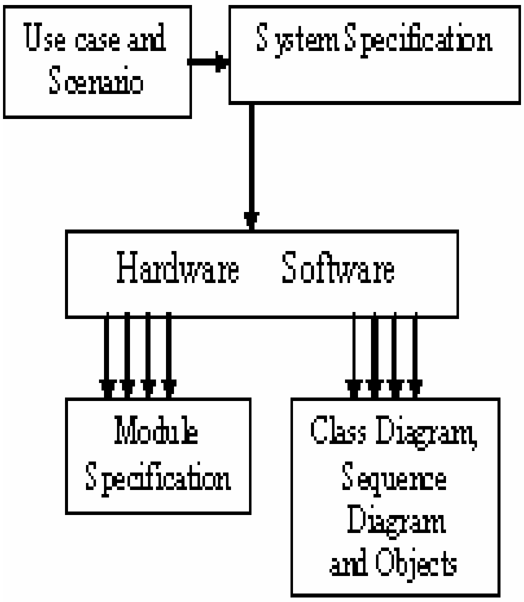

Fig. 5. Decomposition of SDR

Hardware Functional Block:

The principal squares of a genuine SDR are to be specific
(a) Intelligent antenna (b) Programmable RF module (c) High-performance DAC and ADC (d) digital signal processing techniques and (e) Interconnect Technology (Dickens *et. al.* 2008)

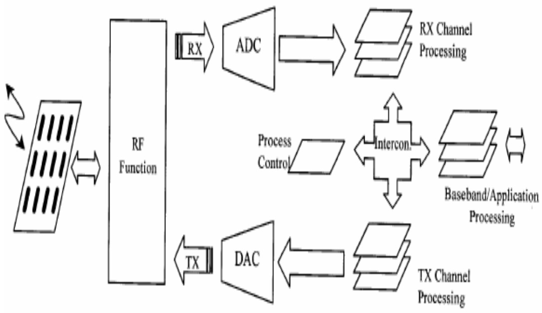

Fig.6. Hardware decomposition of SDR

Intelligent Antenna Technology

Canny Antenna Technology: The present reception apparatus answers for SDR framework depends on a few separate radio wires to cover an expansive scope of recurrence. The primary action in this space remains principally in exhibit preparing squares and procedures to make the receiving wire framework more performing and shrewder. A perfect reception apparatus for a SDR is a self-adjust, adjust, and self-recuperating radio wire, which is fit for complete adjustment to its necessary application and the transmission situations. Small scale Electro-Mechanical System (MEMS) raises the expectation for significant advancements in the field of broadband reconfigurable reception apparatus plan. By utilizing MEMS switches, as appeared in Fig. 5, to reconfigure the receiving wire for another recurrence band it is just required to switch in or out at various opening components.

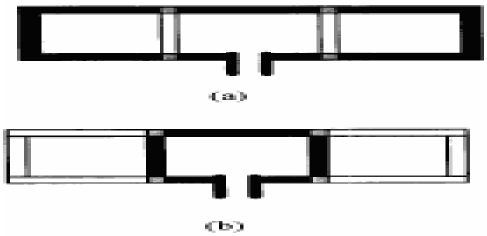

Fig.7. Application of MEMS switch for reconfigurable antenna

2.4 Programmable RF Modules

One of the utilized methods for existing SDR framework is to utilize a bank of RF modules to cover the whole recurrence band. Because of low misfortune and minimization of MEMS innovation, usage of superior RF gadgets with a significant level of combination of circuits including switch have been made conceivable. MEMS innovation improves the exhibition and adaptability of a few RF segments, for example, (Reed 2002) (a) Low stage commotion voltage-controlled oscillators (VCO) by utilizing MEMS-based high Q-resonators (b) Wideband varactors and stage shifters by utilizing MEMS-based variable capacitors and switch capacitor arrange and (c) Tunable channels by utilizing MEMS-based variable responsive components and switches.

2.5 DAC and ADC

SDR framework depends on ADCs and DACs segments. They have the one-of-a-kind assignment of transformation between simple to advance and the other way around. The adaptability of SDR can be expanded fundamentally by pushing the converter closer to the radio wire. ADC execution innovation dependent on the traditional semiconductor approach is answered to accomplish 6 bits goals at 3.2 GS/s and 10 bits goals at 1GS/s. The used semiconductor innovation depends on GaAs supporting at ft =80 GHz. Additionally, the most performing DAC to this date has capacity 12 bits goals at a testing pace of 1.3 GHz.

Table-1 ADC Technology Comparison Chart

ADC Technology	Resolution	Speed	Status
Semiconductor Based	6 Bits	3.2 GS/s	Commercially Available
Optical Sampling	8.2 Bits 12 Bits	505 Ms/s, 12 Bits	Experimental Proof of concept
Superconductor (RSFQ)	11 Bits	175 MS/s	Experimental

Digital Signal Processing Techniques

DSP is the rule empowering component of SDR. Implanted DSP calculation in the handling motor is dependable to make all the guarantees of SDR materialize (Principe 1991 and Tribble 2008). Compression,noise cancellation, multidimensional sifting, adaptive preparation, discovery, estimation, and exhibit handling are barely any zones that have a significant effect on a few applications. Among a few DSP procedures utilized in the SDR stage, testing method, rate change, and multi-rate preparation have been instrumental in the progress and advancement of DSP based framework (Hentschel and Fettweis 2000).

Interconnect Technology

One of the fundamental advantages or targets of the software defined radio is the capacity to interface a few free structure squares to set up a radio connection. A fruitful interconnect approach needs to address following basic issues (a) Open guidelines (b) Addressing various conventions (c) Meeting speeding up and throughput necessities and (d) Connecting to conventional circuit systems .For the most part, there are three primary interconnect designs: transport engineering, switch texture engineering, and tree design (Shafer, and Buck 1999).

Table-2 Interconnection Architecture Comparison chart

	Speed	**Comp-lexity**	**Scala-bility**	**Applica-tion**
Bus	Slow	Low	Low	Medium
Switch Fabric	Medium	Medium	High	High
Tree	Fast	High	Medium	Low

Table 2 summarizes the basic features and disadvantages of each interconnect design.

SOFTWARE FUNCTIONAL BLOCK

Like any product program, programming radio requires quick and proficient different words, radio structure has moved from an emphasis on electromagnetism and hardware to attention on PC designing. The software defined radio can actualize numerous channels and numerous conventions. Equipment radio of the past was regularly single channel and single convention gadgets. SDR is unpredictable to "do everything" radioThe progressions can be assembled in three zones (Kwentus *et. al.*1997) (a) Change in nature of the structure procedure and creators (b) Major growth capability and multifaceted nature and (c) Major increment in the rate at which change happens.

Multirate Digital Signal Processing Code Generation for SDR

When the SDR framework is structured, the calculation must be coded for DSP tasks. Code Composer Studio is a key segment of TI's express DSP Real-Time Software Technology. Intended to slice programming improvement time down the middle and increment the DSP calculation ten times. In this paper codes for Multirate Digital Signal Processing have been written in MATLAB and afterward associated with DSP TMS320C6711 utilizing Code Composer Studio V 3.1. Interpolation and decimation are two procedures of Digital Signal Processing by which we can adjust the examining pace of the given sign without changing the computerized mark of the sign (Milis 2009).

The process of sampling rate alteration by ratio of two integers

In this program alteration of sampling rate of the signal is done by the ratio of two numbers say L and M. It’s a type of Up-Sampling Process. It gives an output sequence $x_u[n]$ with a sampling rate that is L/M times larger/smaller than that of the input sequence x[n].

For this purpose, first get the frequency of the signal and the original frequency of the sampling process and two numbers L and M. Now set the counter having value of N-1 decrease the counter and plot the input signal. Now again declare a variable having value of (L/M)N-1 decrement the counter and store the value of sequence $x_u[n]$ until the value of this variable becomes to zero now plot the output sequence $x_u[n]$. The sampling frequency is increased/decreased by the factor L/M. This Program inserts/deletes a value which between two digital spikes of the signal (Fig. 8(a, b)).

Flow chart for down sampling Process

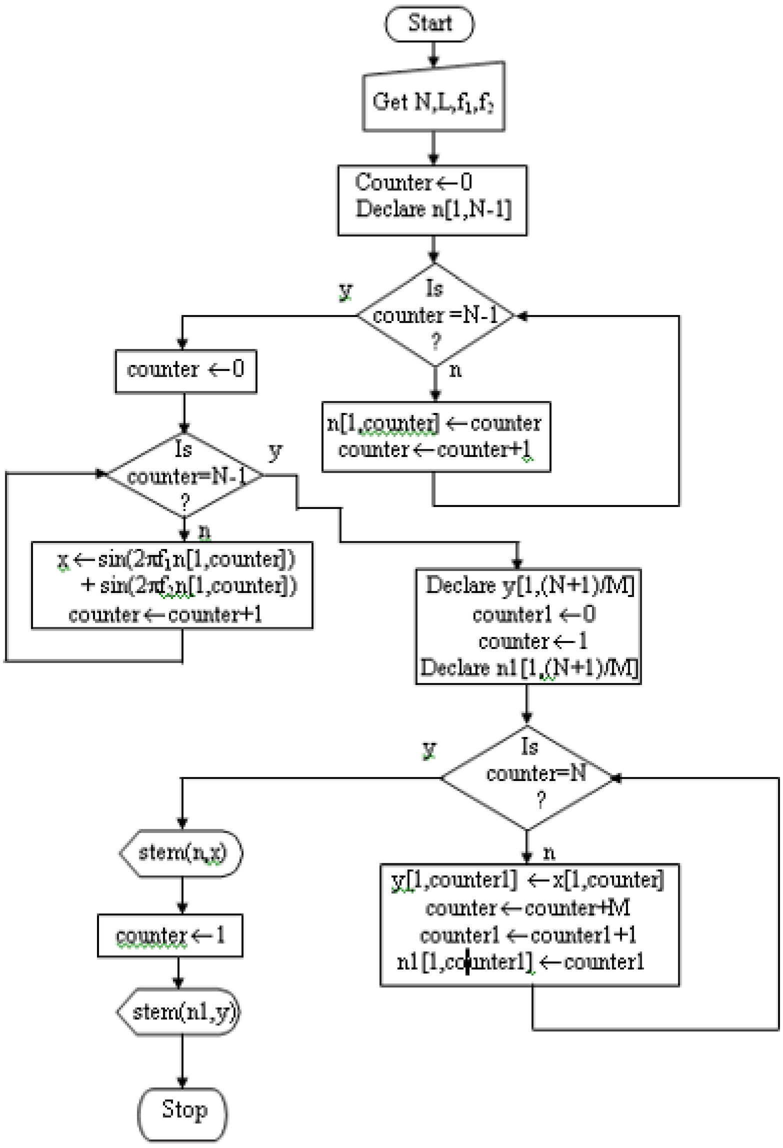

Fig. 8 (a) The down sampling process

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Flow chart for up sampling process

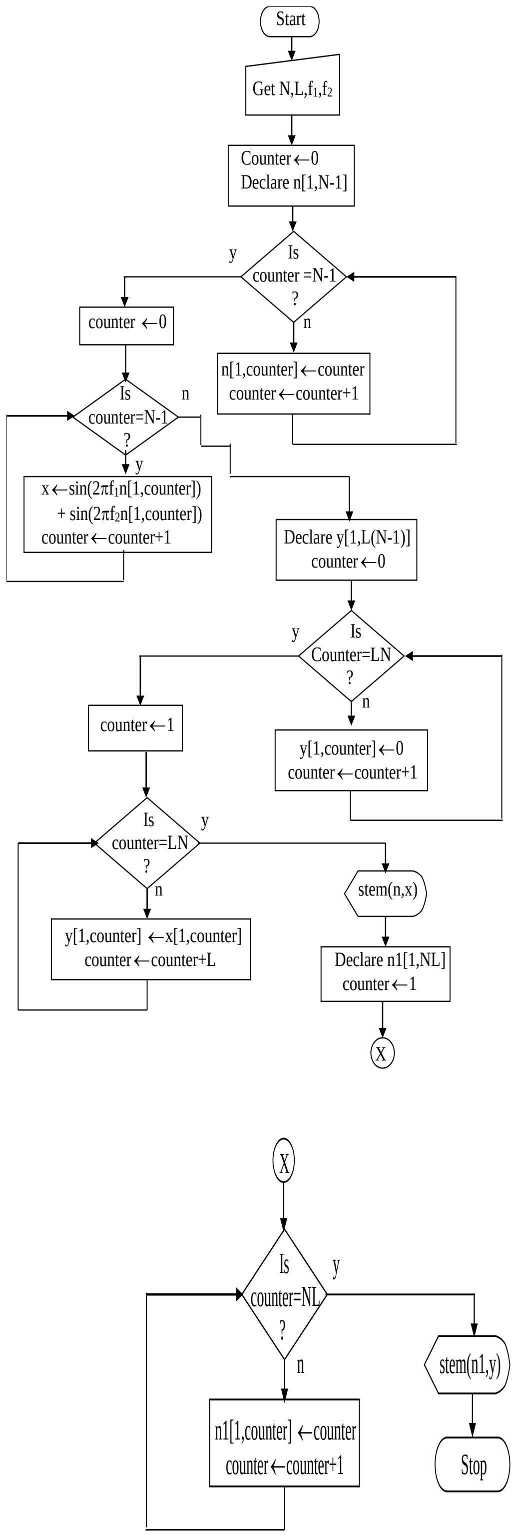

Fig. 8 (b) The up-sampling process

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RESULTS AND DISCUSSIONS

In this segment of the proposition, the outcomes have appeared. As a matter of first importance down sampling and the impact of down sampling are determined after that up sampling and the impact of up sampling are determined. This estimation is trailed by the interpolation and decimation process separately. A consequence of the cascade comb integrator filter is given after the destruction procedure followed by polyphase deterioration and filter bank design. A lot of results have been given below.

Result of down sampling

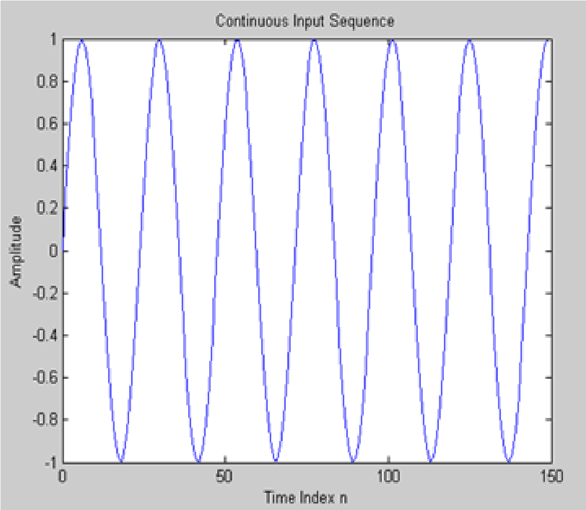

Fig. 9. Continuous Input sequence for down sampling

In Fig. 9. A continuous input sequence is displayed for down sampling. In the next Fig. 10. the continuous input signal is digitize by multiplying the signal with the impulse train. Now this signal of Fig. 10. is used for further operation of down sampling.

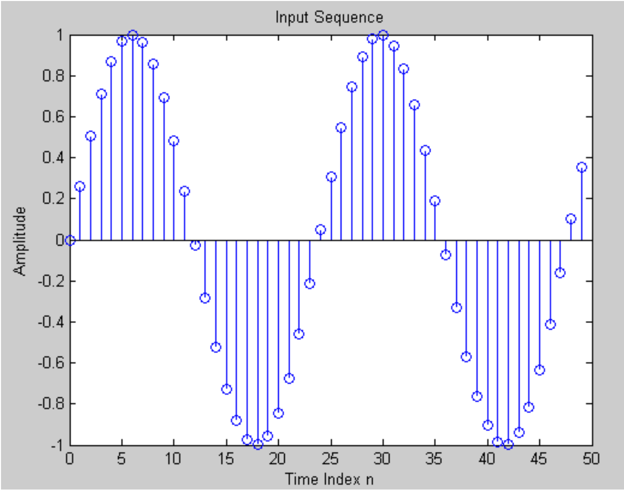

Fig. 10. Digital input sequence for down sampling

[Kaur* 5(12): December, 2018] ISSN 2349-4506
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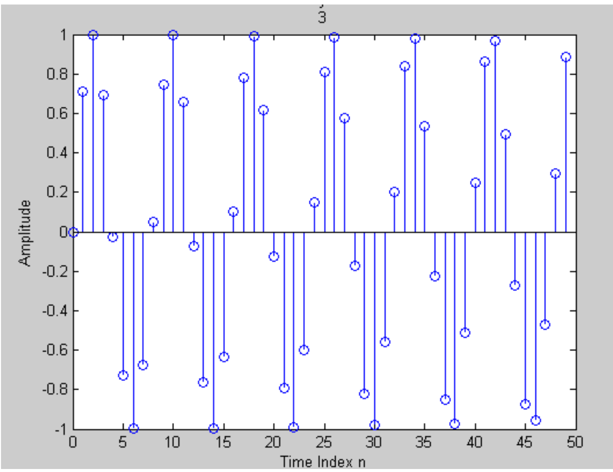

Fig.11. Digital output sequence after down sampling

In Fig 11 a digital output sequence is displayed after down sampling. In the next Fig.12. the continuous output signal is displayed. If we compare the input continuous signal of Fig.9. with the continuous output signal of Fig.12. one can say that there is no significant change in the signature of the signal. By viewing the Fig.10. and Fig.11. one can distinguish the difference between the sampling rates.

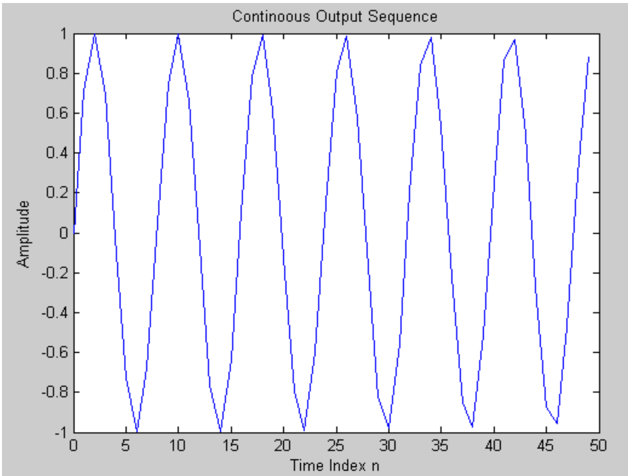

Fig 12 Continuous output sequence after down sampling

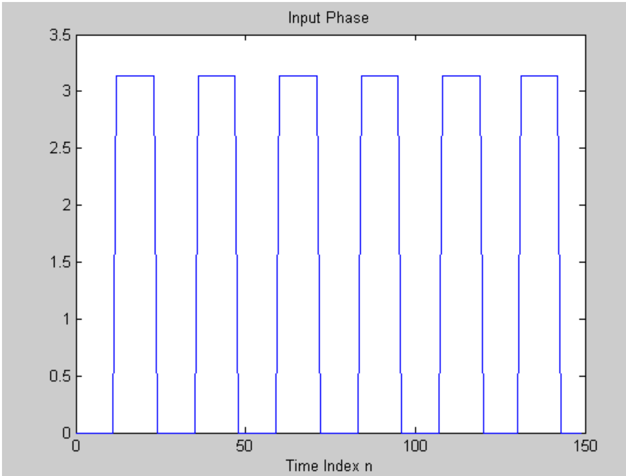

Fig.13. Phase response of input sequence before down sampling

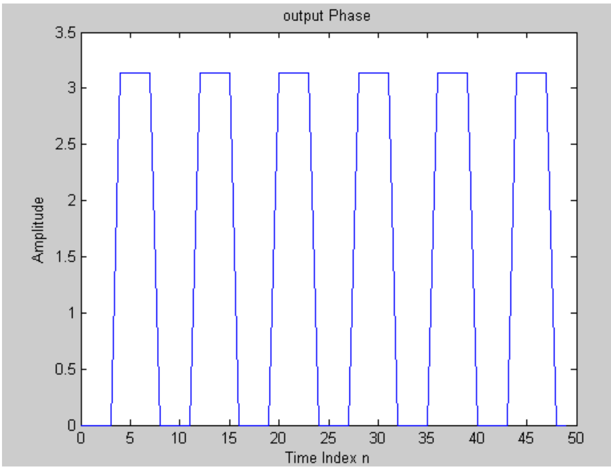

Fig.14. Phase response of output sequence after down sampling

Result of effect of down sampling

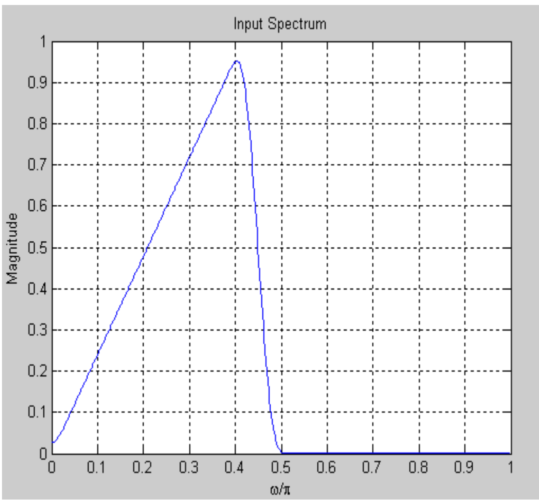

Fig. 15. Frequency spectrum before down sampling

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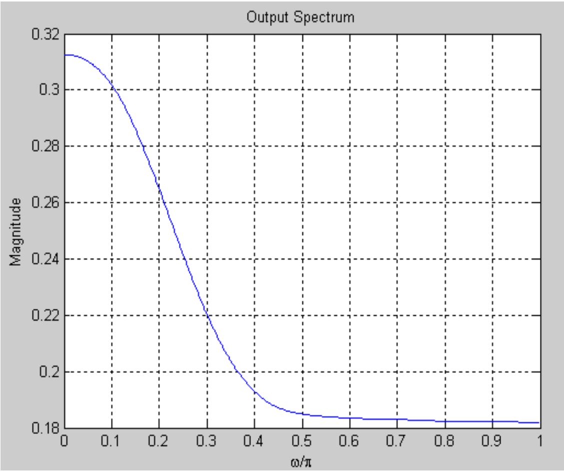

Fig 16. Frequency spectrum after down sampling

In Fig. 15. and Fig. 16. effect of down sampling has been shown. Fig. 15. shows the frequency spectrum of input signal and Fig. 16. shows frequency spectrum of down sampled signal. It is clear from these two graphs that a boarding of frequency spectrum has been occurred after down sampling.

Result of up sampling

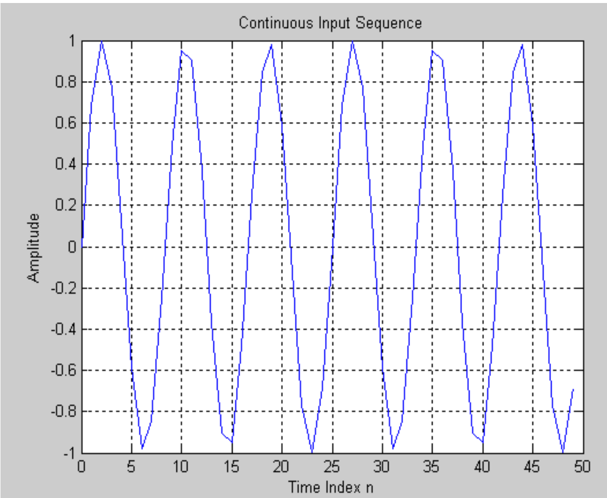

Fig 17. Continuous input sequence for up sampling

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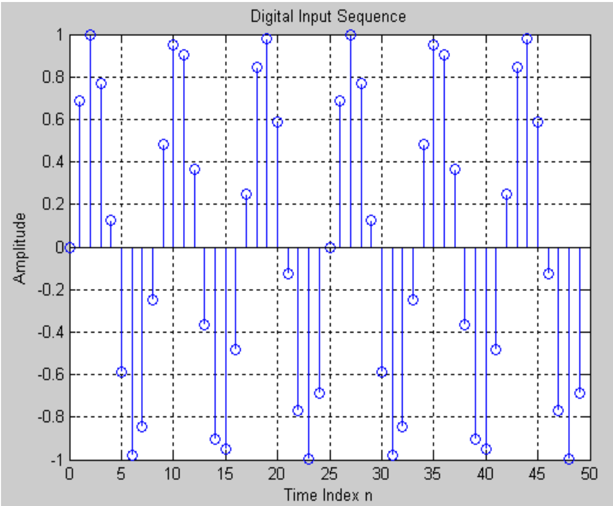

Fig 18. Digital input sequence for up sampling

In Fig 17 a continuous input sequence is displayed for up sampling. In the next Fig 18. the continuous input signal is digitizing by multiplying the signal with the impulse train. Now this signal of Fig 18 is used for further operation of up sampling.

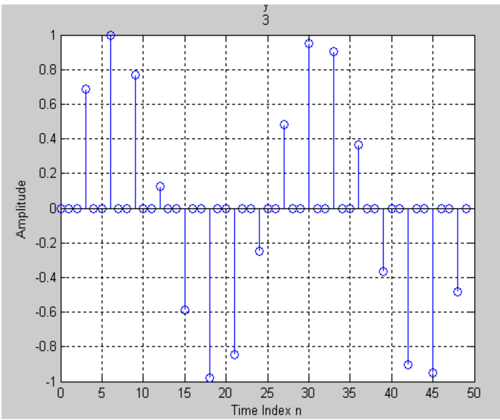

Fig 19. Digital output sequence after up sampling

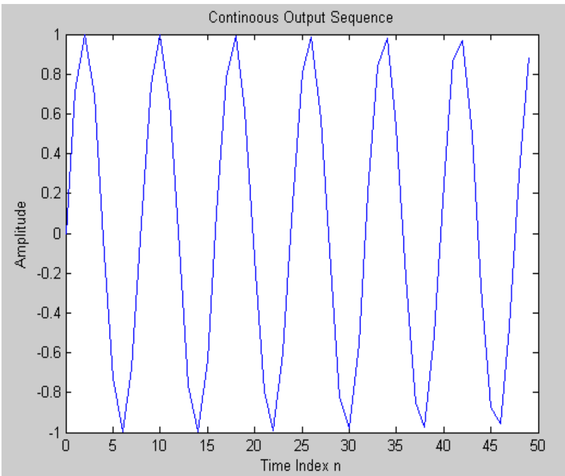

Fig 20. Continuous output sequence after up sampling

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In Fig.19. a digital output sequence is displayed after up sampling. In the next Fig. 20. the continuous output signal is displayed. If we compare the input continuous signal of Fig. 17. with the continuous output signal of Fig. 20. one can say that there is no significant change in the signature of the signal. By viewing the Fig. 18 and Fig. 19. one can distinguish the difference between the sampling rates.

In Fig. 21 and Fig 22. a graph of phase of input signal and the phase of output signal has been plotted respectively. By reading these two graphs one can say that there is no change in the phase of the signal after up sampling.

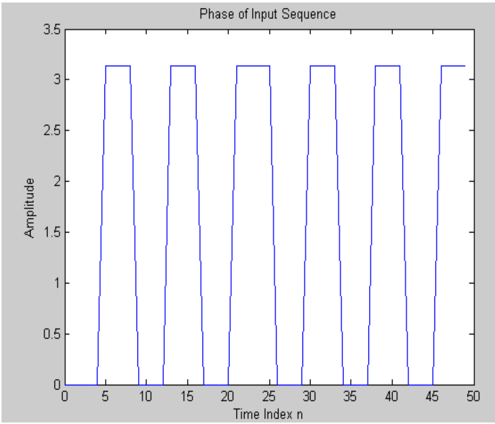

Fig.21. Phase response of input sequence before up sampling

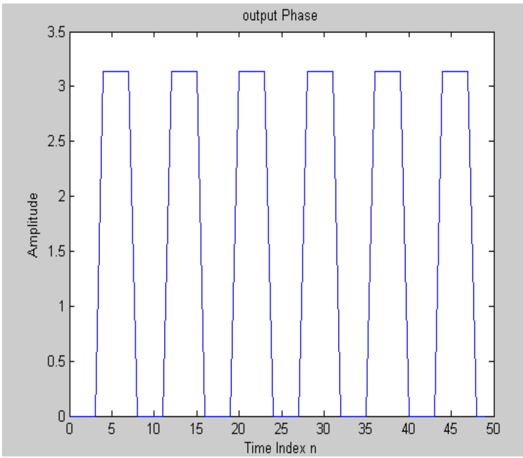

Fig.22. Phase response of output sequence after up sampling

Result of effect of down sampling

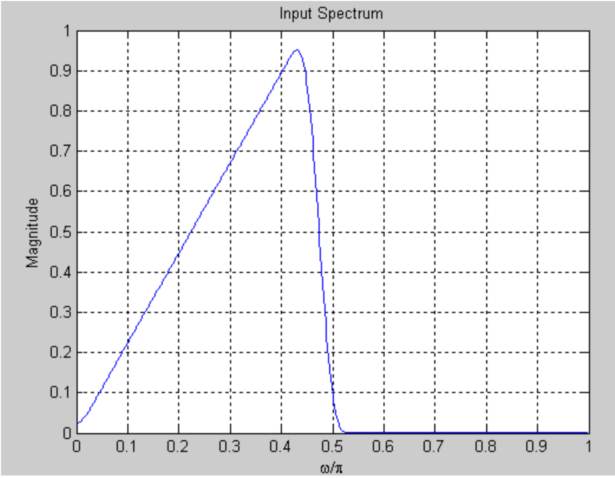

Fig. 23. Frequency spectrum before up sampling

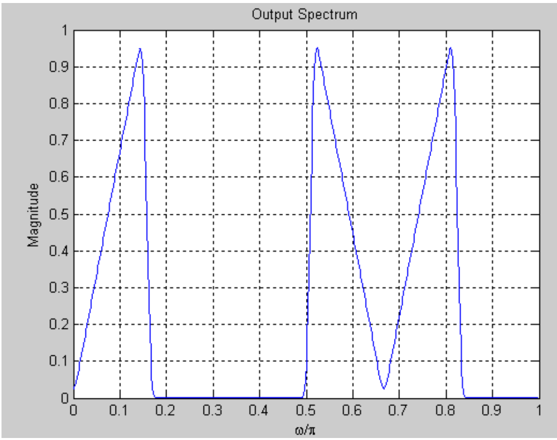

Fig. 24. Frequency spectrum after up sampling

In Fig. 23 and Fig.24. effect of down sampling has been shown. Fig. 23. shows the frequency spectrum of input signal and Fig.24. shows frequency spectrum of up sampled signal. It is clear from these two graphs that a there is formation of more lobes of frequency spectrum has been occurred after up sampling.

CONCLUSION

The amusement system for coding and interpreting of these codes was performed with MATLAB (R2017a) programming. The testing of the structure has been refined for signs having an alternate repeat. This item system has been gone after for various shape, sufficiency, and length of sign. The current programming will be progressively relevant when the choice for hardware realized analyzing contraption related considering the requirement of simple to computerized converter availability. This item will suit the sign when fitting hardware elective isn't available. The item system made is progressively compressive and instinctive as it might be helpfully realized just by changing the factor which is the entire number in nature.

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